

AXIe Supports Cutting-Edge DDR Memory Design-Validation Solution

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Dynamic memories (DDR) continue to become faster and wider with each iteration and DDR4 standard was ratified by JEDEC in September 2012. Design validation solutions for DDR memory designs require using lots of complex ASIC's and FPGA's to provide simultaneous read/write capability at greater than 3 Gb/s rates and scalable to as many bytes wide required. AXIe was the right form-factor to formulate a complex and scalable digital logic analyzer solution.

Richard Stern, Keysight Technologies R&D Manager states "The Keysight U4154B module contains a large number of high power custom ASICs, enabling it to be the only logic analyzer in the world to capture DDR4 and LPDDR4 data at 3.2Gb/s data rate. The large board AXIe form factor allows for a coplanar design which preserves signal integrity, and also provides the power and cooling required by this complex module. In addition, the two and five slot chassis allows us to easily scale the digital channel count of the measurement system up or down to solve a wide variety of customer challenges, from a single DRAM to a full DDR4 DIMM interposer solution."

For the EE Times interview, "Memory Trends: A Look Ahead by Janine Love", go to

http://www.eetimes.com/document.asp?doc_id=1325195&_mc=SM_ELE_editor_JanineLove.